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DATE: June 9, 2004

FILE #: 20193/0800797/030

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703-308-7751	The United States Patent and Trademark Office Corrected Filing Receipt Division	NO

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Please see attachments.

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Customer No.: 07278

Docket No.: 20193/0200799US0

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Juergen Freiwald et al.

Serial No.: 10/746,108

Filed: January 16, 2004

For: METHOD FOR CONTROLLING A CENTRAL PROCESSING UNIT FOR ADDRESSING
 IN RELATION TO A MEMORY AND CONTROLLER

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner of Patents and Trademarks
 P.O. Box 1450
 Alexandria, VA 22313-1450

Sir:

- Attached is a copy of the official filing receipt received from the PTO in the above application for which issuance of a corrected filing receipt is respectfully requested.
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Error in

Correct data

- ☐ Applicant's name
☐ Applicant's address
☒ Title

Method for controlling a central processing unit for
addressing in relation to a memory and controller

- ☐ Filing Date
☐ Application Number
☐ Foreign/PCIT Application Re:
☐ Other

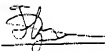
Remarks:

Enclosed please find a copy of the Executed Declaration.

The Commissioner is respectfully requested to issue a new and correct Filing Receipt.

Respectfully submitted,

Dated: June 8, 2004



Flynn Barrison
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APPL NO.	FILING OR M/ (3) DATE	ART UNIT	FIL REC REC'D	ATTY DOCKET NO.	DRAWINGS	TOT CLMS	IND CLMS
107/60,108	01/16/2004	2183	0.0J	20046/0200799-USO	5	28	4

CONFIRMATION NO. 7345

07278

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FILING RECEIPT



OC00000001242996

Date Mailed: 04/23/2004

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Applicant(s)

Juergen Freiwald, Eggenheim, GERMANY;
 Dirk Rabe, Munchen, GERMANY.

Assignment For Published Patent Application

Infineon Technologies AG, Munich, GERMANY.

Domestic Priority data as claimed by applicant

This application is a CON of PCT/EP02/06655 06/17/2002.

Foreign Applications

EUROPEAN PATENT OFFICE (EPO) 01117389.5 07/18/2001

If Required, Foreign Filing License Granted: 04/22/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

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Title

Method for controlling a central processing unit for an addressing relating to a memory and

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controller

Preliminary Class

712

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